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2811
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11-18-03
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TOLSON MAIL ROOM

In-re the Application of:

TI-31359

Gerard Chauvel, et al.

Art Unit: 2811

Serial No: 09/932,137

Examiner:

Filed: August 17, 2001

Conf. No.: 5164

For: Dynamic Hardware Control for Energy Management Systems Using Task Attributes

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

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Robin E. Barnum

Dear Sir:

Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO-1449. Copies of the noted references are enclosed herewith. The relevancy of each of the references is noted on the enclosed Search Reports.

Since this IDS is being submitted before any Office Action, no fee is due.

Respectfully submitted,



Robert D. Marshall, Jr.
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1 of 1

Complete If Known

Application Number	
Filing Date	August 17, 2001
First Named Inventor	Gerard Chauvel, et al.
Group Art Unit	2811
Examiner Name	
Attorney Docket No.	TI-31359

U.S. PATENT DOCUMENTS

Exam. Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
	AA	5,339,445 A		Laszlo R. Gasztonyi	8/16/94	
	AB	5,623,647 A		Amit K. Maitra	4/22/97	
	AC					
	AD					
	AE					
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	AG					
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FOREIGN PATENT DOCUMENTS

Exam. Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ² (if known)				
	BA	EP	0 683 451 A		Canon KK	11/22/95		
	BB	EP	0 794 481 A		Mitsubishi Elec. Corp.	9/10/97		
	BC							
	BD							
	BE							
	BF							
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Exam. Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	CA	R. Young, et al.; <i>Adaptive Clock Speed Control for Variable Processor Loading</i> , Motorola Technical Developments, Motorola, Inc., Schaumburg, IL, US, Vol. 15, May 1, 1992, pgs. 43-44.	
	CB	I.A. Manzak, et al.; <i>Variable Voltage Task Scheduling for Minimizing Energy or Minimizing Power</i> , 2000 IEEE Int'l Conf. on Acoustics, Speech & Signal, Vol. 6, June 5, 2000, pgs. 3239-3242.	
	CC		
	CD		
	CE		
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	CG		
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Examiner Signature		Date Considered	
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